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## APPLICANTS

Hajime Yamamoto, Kawasaki, JAPAN;

Satoshi Takechi, Kawasaki, JAPAN;

 \*\* CONTINUING DATA *none ny* \*\*\*\*\*

 \*\* FOREIGN APPLICATIONS *ny* \*\*\*\*\*  
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>ny</i> Initials				

## ADDRESS

38834  
 WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP  
 1250 CONNECTICUT AVENUE, NW  
 SUITE 700  
 WASHINGTON, DC  
 20036

## TITLE

Method of manufacturing semiconductor device, and method of forming resist pattern

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